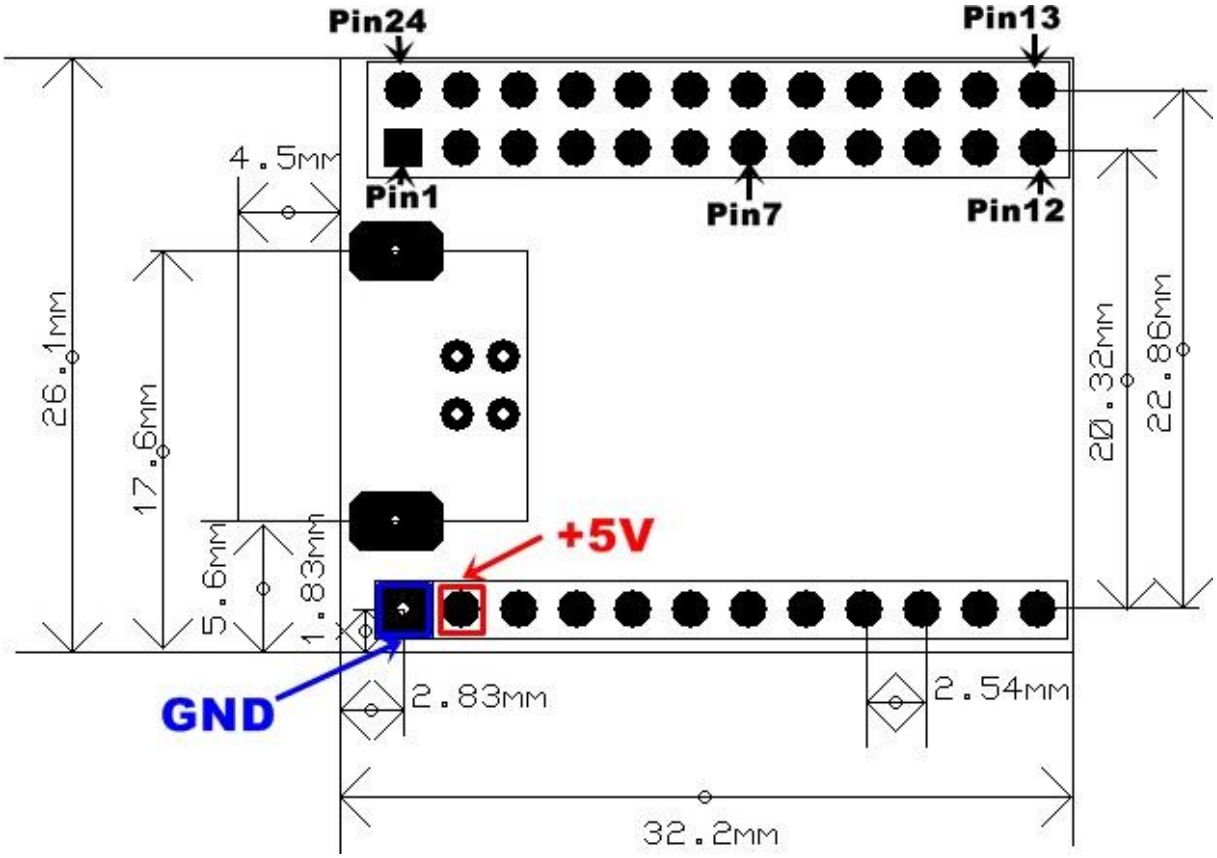

Pin #	Signal
1	BCLK I2S / CLK DSD
2	S/PDIF output for direct control of TOTX
3	GND
4	DATA_I2S / DATA_L DSD
5	MCLK input (22.5792MHz / 24.576MHz)
6	LRCK_I2S / DATA_R DSD
7	DATA FLAG – +3.3V – DSD data; 0V – PCM data
8	A0 // MCLK_select – 0V – 22.5792MHz; +3.3V – 24.576MHz
9	No connect
10	CODEC RESET 0V – reset; 3.3V – work
11	No connect
12	No connect
13	No connect
14	No connect
15	A2
16	A1
17	No connect
18	No connect
19	GND
20	GND
21	GND
22	3.3V output (20mA)
23	GND
24	GND

DSD FLAG	A2	A1	A0	Output
0	0	0	0	44.1kHz
0	0	0	1	48kHz
0	0	1	0	88.2kHz
0	0	1	1	96kHz
0	1	0	0	176.4kHz
0	1	0	1	192kHz
0	1	1	0	352.8kHz
0	1	1	1	384kHz
1	1	0	X	DSD64
1	1	1	X	DSD128

If one wants to use external power supply, USB jumper have to be opened and to provide power supply on the +5V and GND pins (140mA (without additional loaded outputs)). The power supply must power up below 20ms. If the jumper is installed this is +5V output, from the USB host.

If one uses 22.5792MHz and 24.576MHz oscillators, the reclock of 352.8kHz, 384kHz and DSD128 can’t be done.

If one wants to have reclock and for those sample rates, then have to use 45.1584MHz and 49.152MHz oscillators, but the frequency have to be divided by 2 before tracing it to the XMOS processor. If one wants to use a galvanic isolation, this should be done on the oscillators side of the barrier (not from the XMOS side).